

	Document ID	Issue Date	Pages	Title	Current OR
1	US 20040037354 A1	20040226	19	Communication receiver with virtual parallel equalizers	375/229
2	US 20040013189 A1	20040122	25	Soft slicer in a hybrid decision feedback equalizer	375/233
3	US 20040001538 A1	20040101	10	Error convergence measurement circuit for providing convergence of a filter	375/229
4	US 20020181555 A1	20021205	10	Method and device for demodulating signals from multiple users	375/147
5	US 6937648 B2	20050830	20	Equalizer for communication over noisy channels	375/229
6	US 6904110 B2	20050607	183	Channel equalization system and method	375/350
7	US 6876699 B1	20050405	18	Filter circuit for a bit pump and method of configuring the same	375/233
8	US 6862326 B1	20050301	25	Whitening matched filter for use in a communications receiver	375/343
9	US 6765958 B1	20040720	17	High-speed adaptive interconnect architecture	375/232
10	US 6618451 B1	20030909	14	Efficient reduced state maximum likelihood sequence estimator	375/341
11	US 6337878 B1	20020108	25	Adaptive equalizer with decision directed constant modulus algorithm	375/229
12	US 6055268 A	20000425	75	Multimode digital modem	375/229
13	US 6038251 A	20000314	75	Direct equalization method	375/222

	Current XRef	Inventor
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